

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: **Raimund Peichl et al.**

Serial No.:

Filed: **December 8, 2003**

Title: **Lateral Pin Diode and Method for
Processing Same**

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Group Art Unit:

Examiner:

Attorney Docket No: **068758.0145**

Client Ref.: **IO20511PUS/gi/pp**

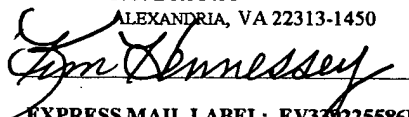
Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING VIA EXPRESS MAIL

PURSUANT TO 37 C.F.R. § 1.10, I HEREBY CERTIFY THAT I HAVE INFORMATION AND A REASONABLE BASIS FOR BELIEF THAT THIS CORRESPONDENCE WILL BE DEPOSITED WITH THE U.S. POSTAL SERVICE AS EXPRESS MAIL POST OFFICE TO ADDRESSEE, ON THE DATE BELOW, AND IS ADDRESSED TO:

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INFORMATION DISCLOSURE STATEMENT

Sir:

Applicants respectfully request, pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, that the art listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified application. A copy of the cited art is enclosed for the convenience of the Examiner.

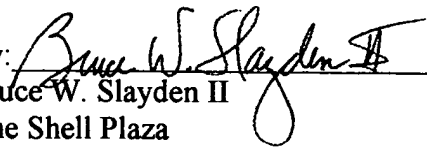
Furthermore, pursuant to 37 C.F.R. §§1.97(g) and (h), no representation is made that these references are material to the patentability of the present application.

As the Information Disclosure Statement is being submitted before the mailing of the first office action on the merits, Applicants believe that no fee is required. If a fee is

required, please accept this transmittal as a petition therefor and charge any fee to Baker Botts L.L.P. (formerly, *Baker & Botts, L.L.P.*) Deposit Account No. 02-0383, Order No. (068758.0145) for any other charges necessary for the filing of this Information Disclosure Statement.

BAKER BOTTS L.L.P. (023640)

Date: December 8, 2003

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PTO-1449 Information Disclosure Citation in an Application		Application No.		Applicant(s): Raimund Peichl et al.			
		Docket Number 068758.0145		Group Art Unit		Filing Date December 8, 2003	

U.S. PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	1	4,571,559	02/18/86	Henry et al.	333	17L	10/11/84
	2	5,343,067	08/30/94	Nakagawa et al.	257	487	01/31/92

FOREIGN PATENT DOCUMENTS								
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	3	WO99/59208	11/18/99	PCT	H01L	29/739	x	
	4	WO01/71819A2	09/27/01	PCT	H01L	29/868	x	
	5	FR 2 632 776 A1	06/10/88	FRANCE	H01L	29/90		x
	6	57128983	10-08-82	JAPAN (ABSTRACT)	H01L	29/91	x	
	7	58134479	10-08-83	JAPAN (ABSTRACT)	H01L	29/91	x	

NON-PATENT DOCUMENTS		
	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
8	Sridhar S., et al.; "Dielectrically Isolated Lateral High Voltage PIN Rectifiers for Power ICs"; Technical Digest, International San Francisco, CA; pps. 245-248	12/92

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.